IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gilbert Wolrich et al. Art Unit: 2183

Serial No.: 09/760,509 Examiner: Aimee J. Li

Filed: January 12, 2001 Assignee: Intel Corporation
Title: METHOD AND APPARATUS FOR PROVIDING LARGE REGISTER

ADDRESS SPACE WHILE MAXIMIZING CYCLETIME PERFORMANCE

FOR A MULTI-THREADED REGISTER FILE SET

MAIL STOP RCE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

This filing is being made with a Request for Continued Examination (RCE). No fee is required.

The documents are in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Applicant : Gilbert Wolrich et al. Attorney's Docket No.: 10559-317001 P9678

Serial No.: 09/760,509

Filed : January 12, 2001

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Please apply any credits or additional charges to deposit account 06-1050.

Respectfully submitted,

Date: __July_9, 2007

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JOHN F. CONROY REG. NO. 45,485

		Attorney's Docket No. Application No. 10559-317001 09/760,509		
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.		
		Filing Date January 12, 2001	Group Art Unit 2183	

U.S. Patent Documents							
Examiner Initial	Desig.	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	4,606,025	08/1986	Peters et al.			
	AB	5,860,085	01/1999	Stormon et al.			
	AC	6,052,769	04/2000	Huff et al.			
	AD	6,934,951	08/2005	Wilkinson, III et al.			
	AE	7,181,594	02/2007	Wilkinson, III et al.			
	AF	7,191,309	03/2007	Wolrich et al.			

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig.	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Trans Yes	lation No

Examiner Desig. Initial ID Heurin	Document Document Analyticature Pending MA Addison Wesley
Heurin	1 C 4 C A Addison Wesley
	g et al., Computer Systems Design and Architecture, Reading, MA, Addison Wesleynan, Inc., pp. 69-71, (1997).
	Gerry, PA-RISC 2.0 Architecture, 1995, Prentice Hall PTR, pp. 1-6, 7-13 & 7-14.

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if no	ot in conformance and not considered. Include copy of this form with